******** Tetsuo Hattori ***

Brief Biography:

He received B.E. and M.E. degrees in electrical engineering from Doshisha University, Kyoto, Japan, and Ph.D. degree in electronics & Information engineering from Osaka University, Osaka, Japan.

From 1988, he had been with Kagawa University as a lecturer, associate professor, and professor at the department of information science in the faculty of education.

Until then he was engaged in developing high-speed image processor and automated visual inspection systems in Toshiba ENG. Co. Ltd., where he invented a world-first high speed regional labeling processor that continuously pipeline processes images at the video rate from industrial TV cameras.

In 1996-1997, he contributed to establish the engineering department in Kagawa University, and moved to the department in 1997. In 2001, he was invited as a guest researcher at the University of Connecticut, Connecticut, USA, for collaborating the study of Quantum Computing. Since 2015, he has been a Professor Emeritus at Kagawa University. Also, he is currently a Guest Professor at Hiroshima Institute of Technology, and a director of Japan Creativity Development Center which is a specified Non-Profit Organization (https://www.jcdc.jp/).